

FIG. 1a
(prior art)

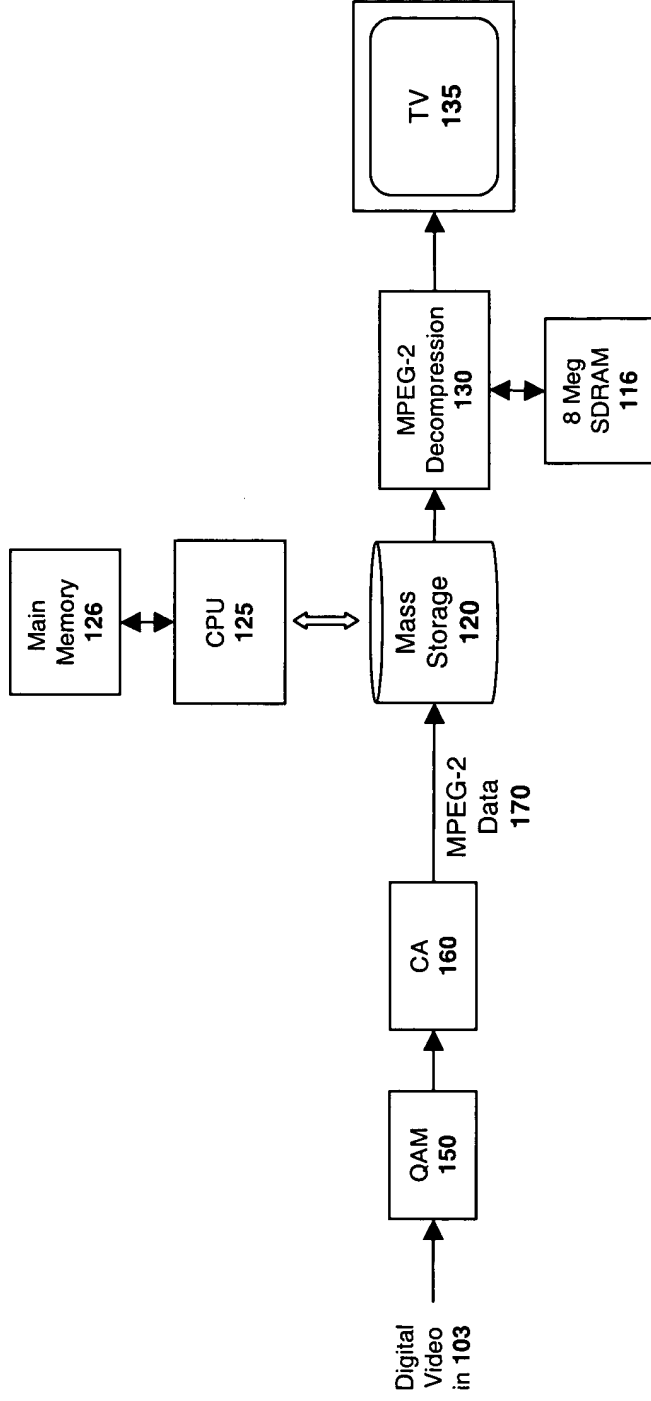


FIG. 1b
(prior art)

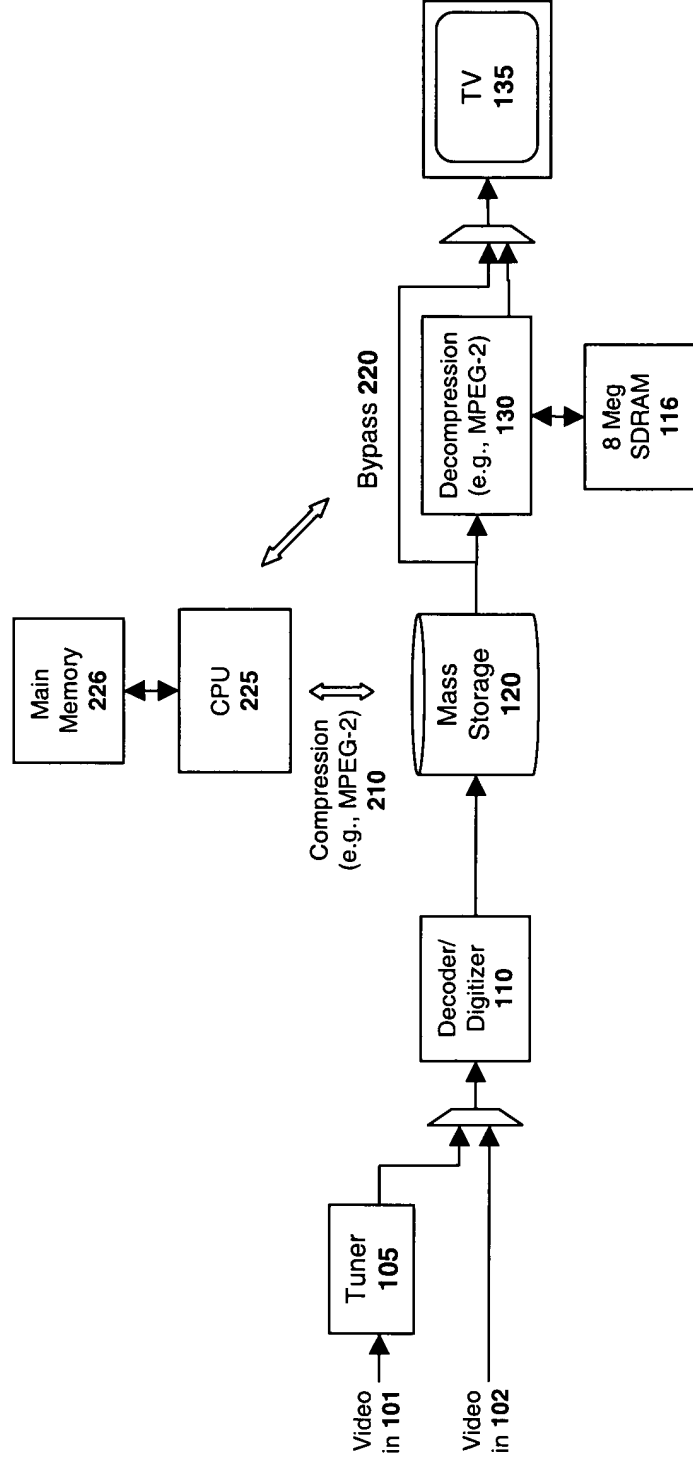


FIG. 2

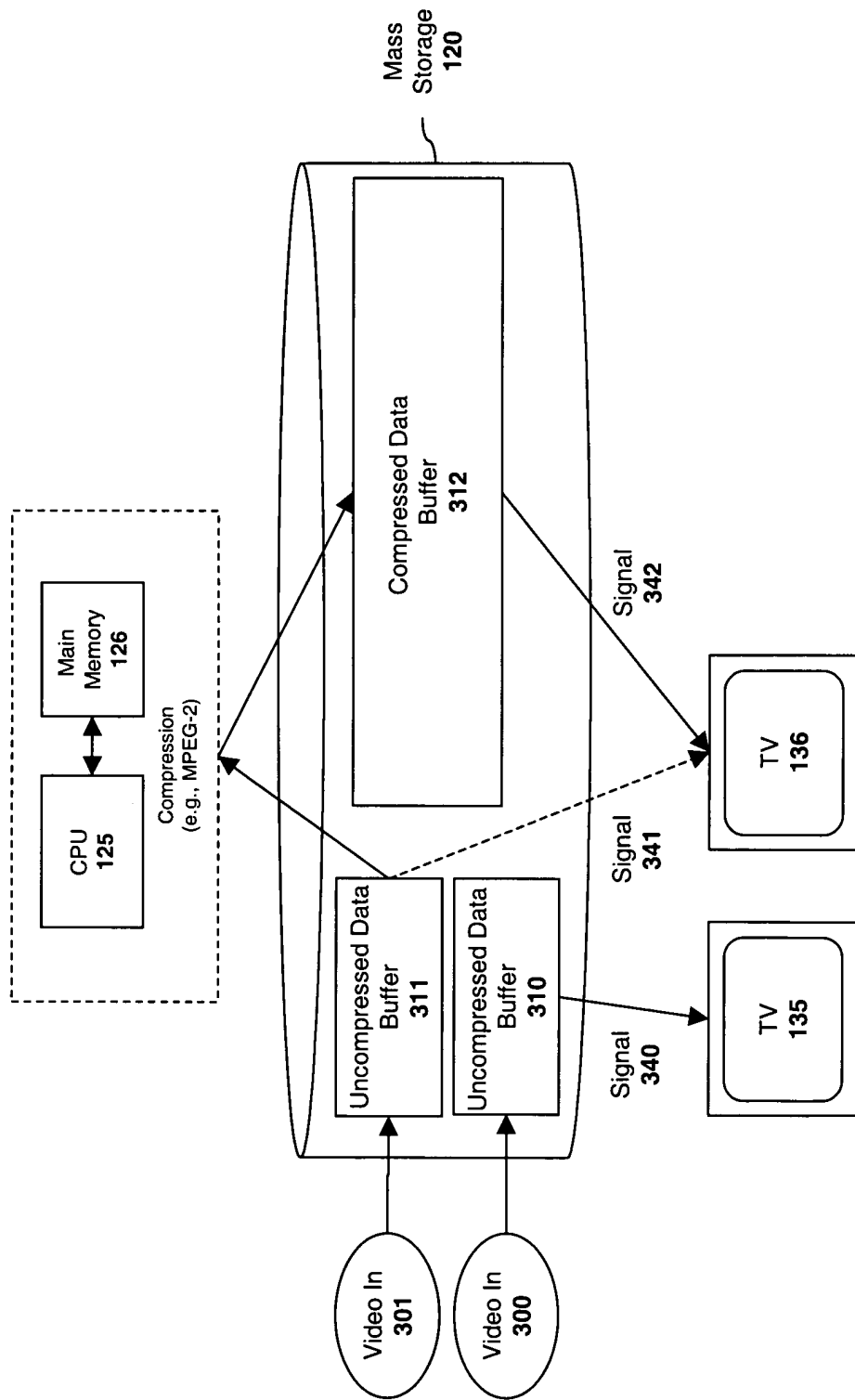


FIG. 3

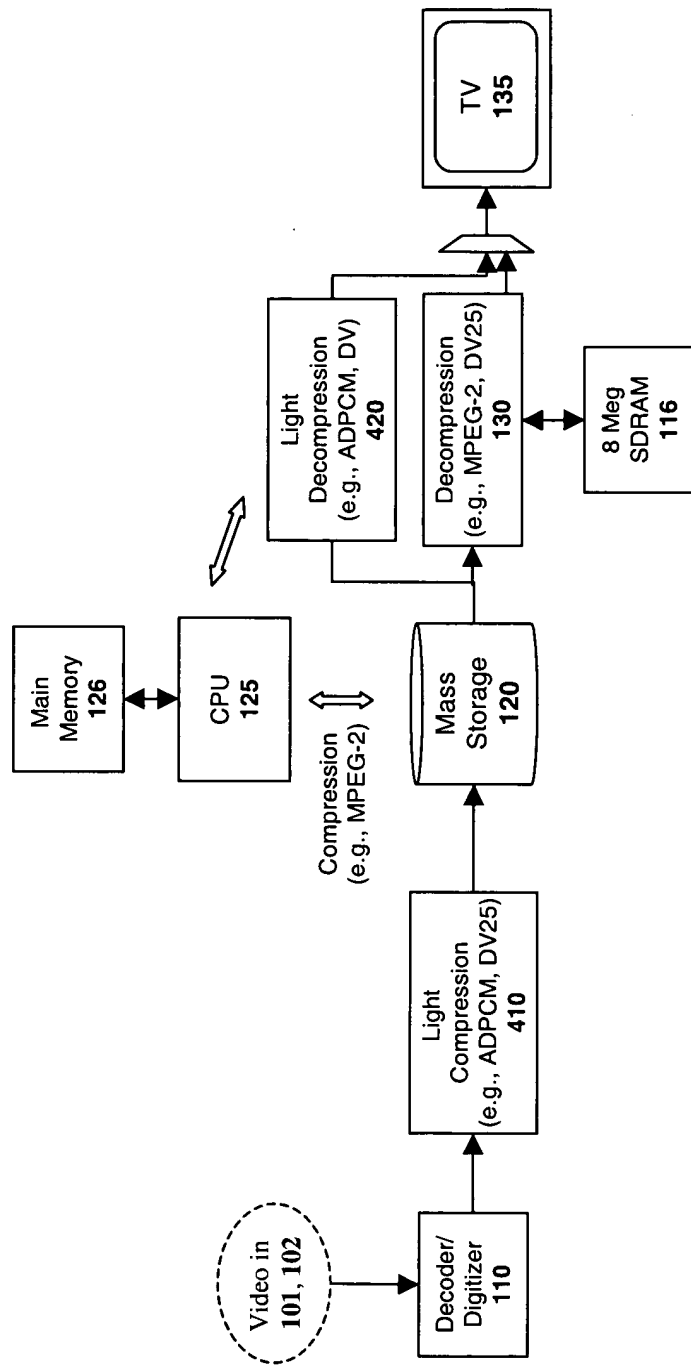


FIG. 4

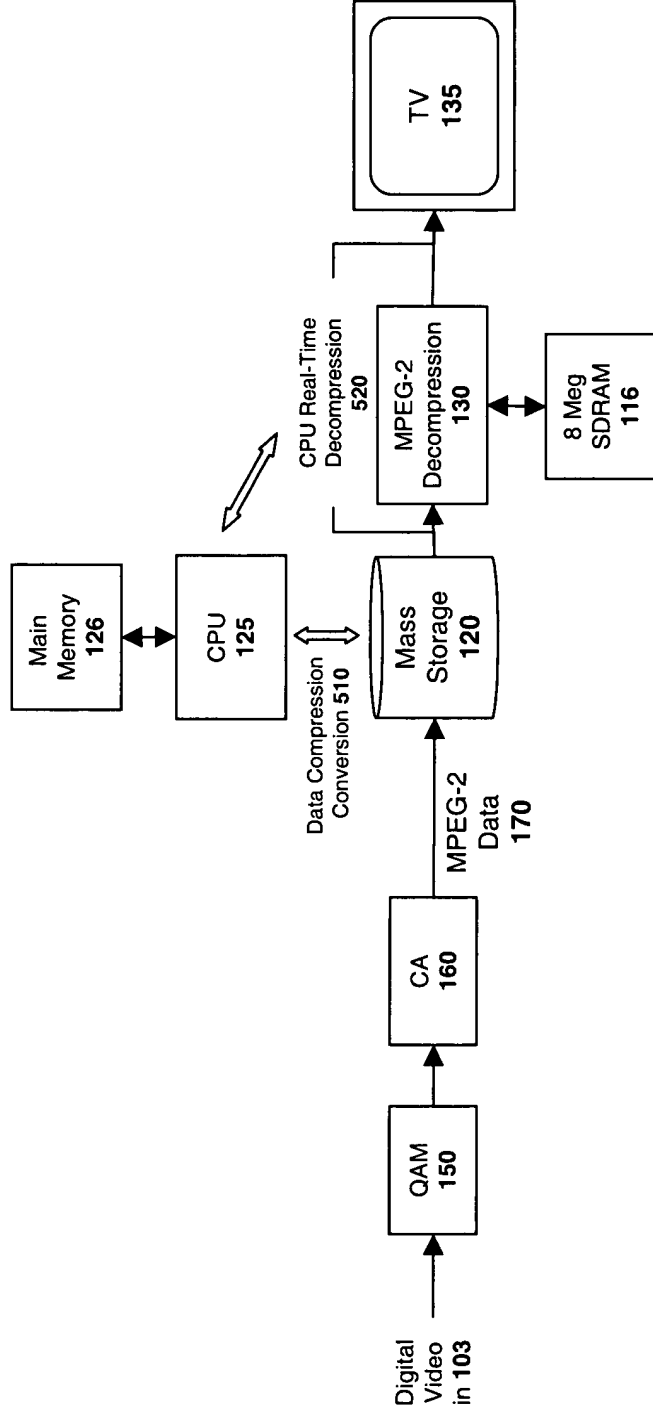


FIG. 5

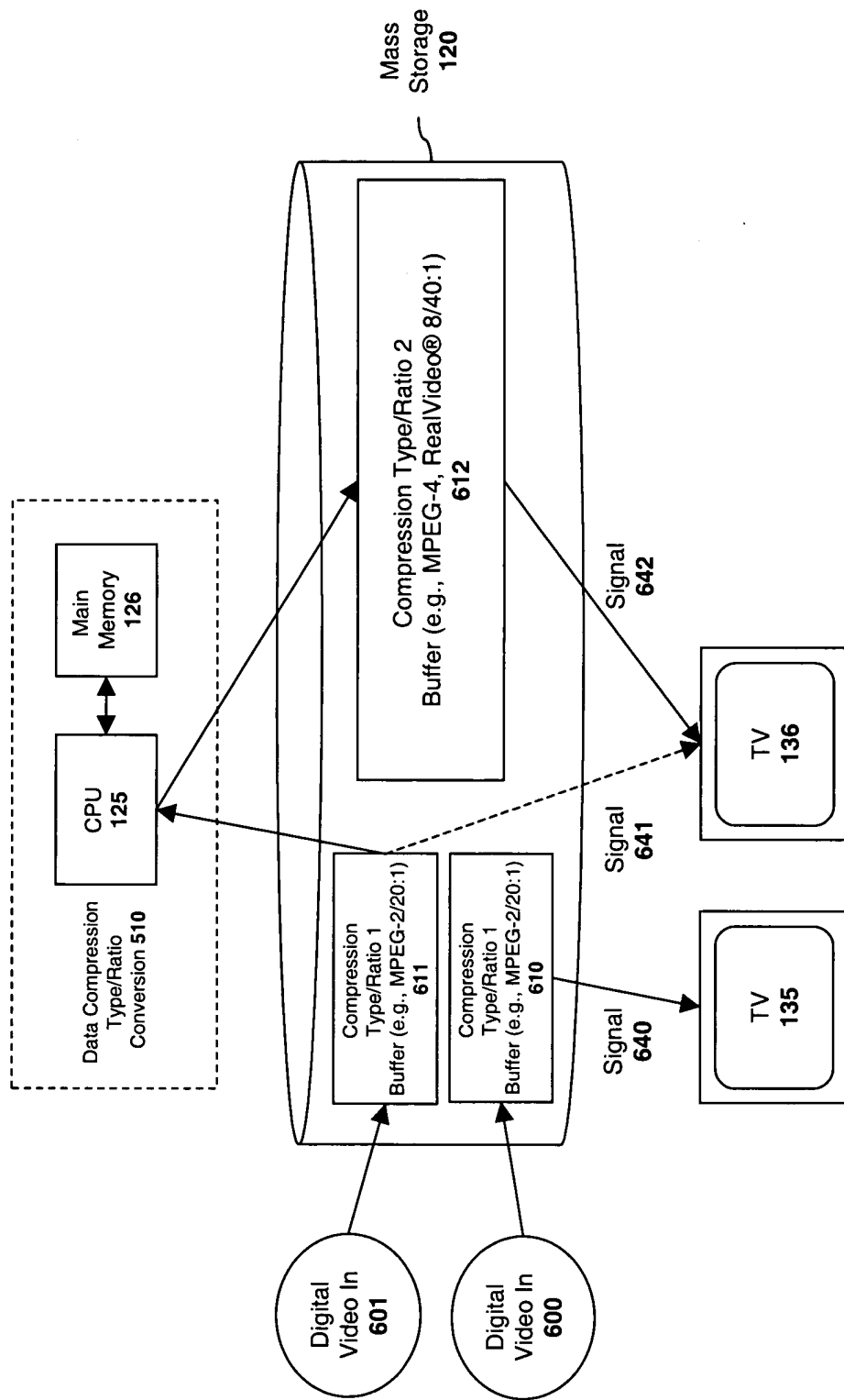


FIG. 6

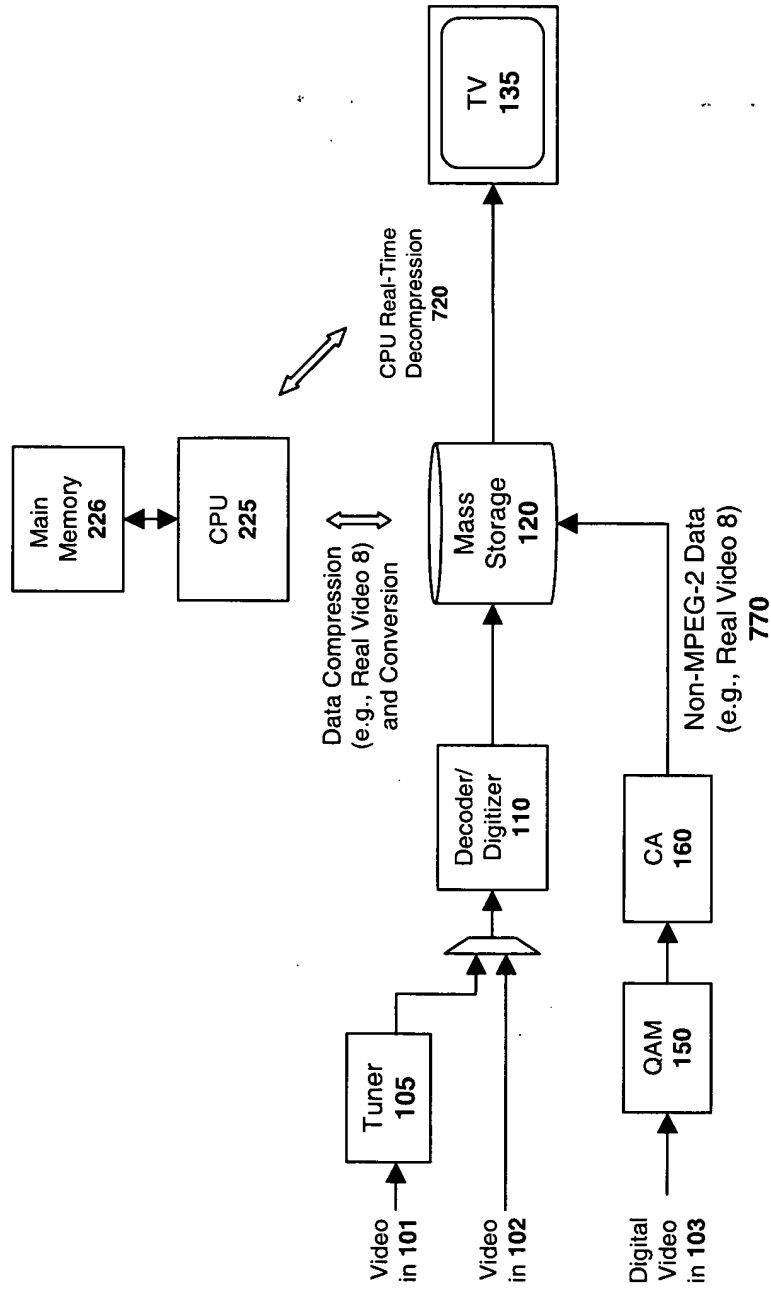


FIG. 7a

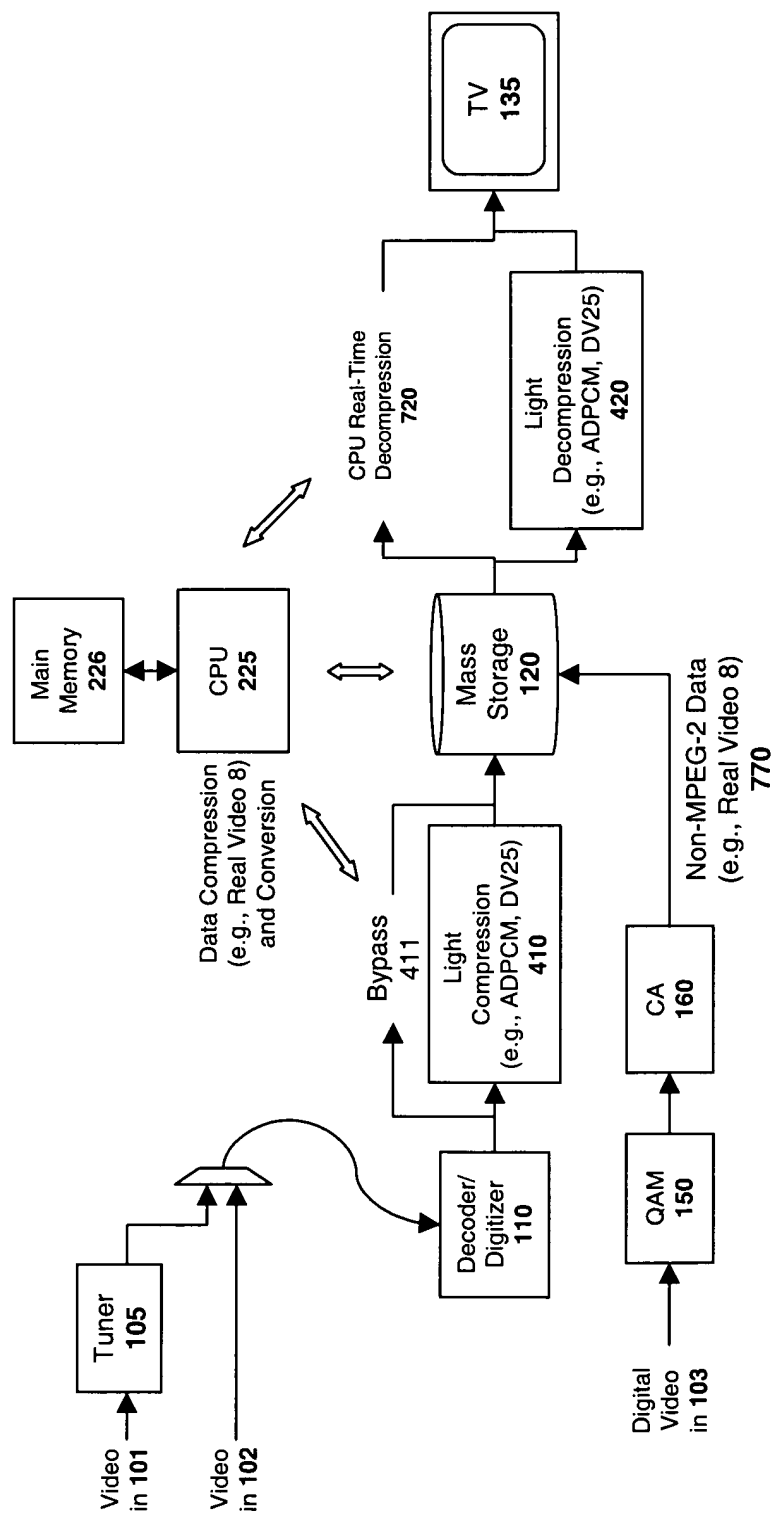


FIG. 7b

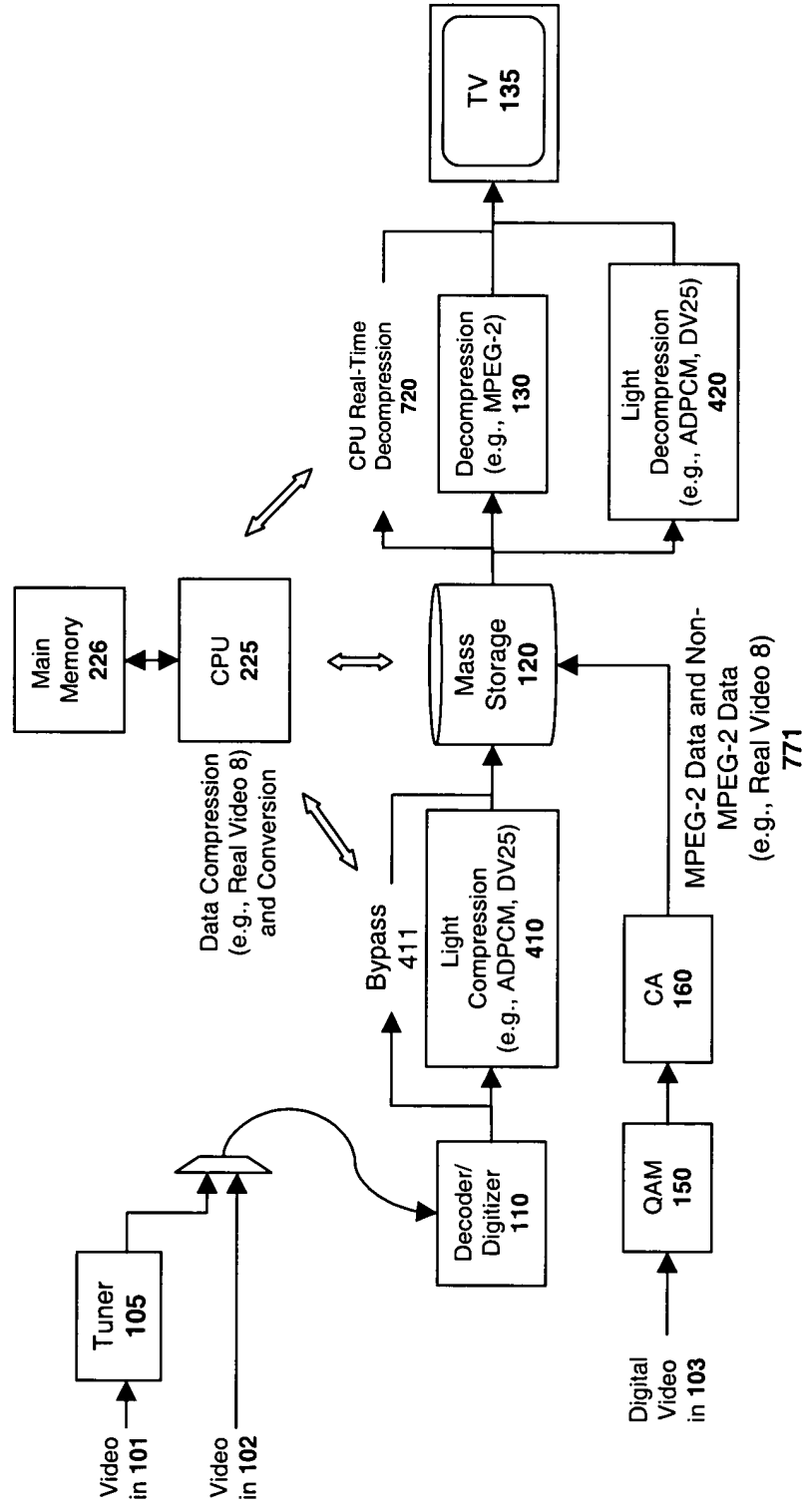


FIG. 7C

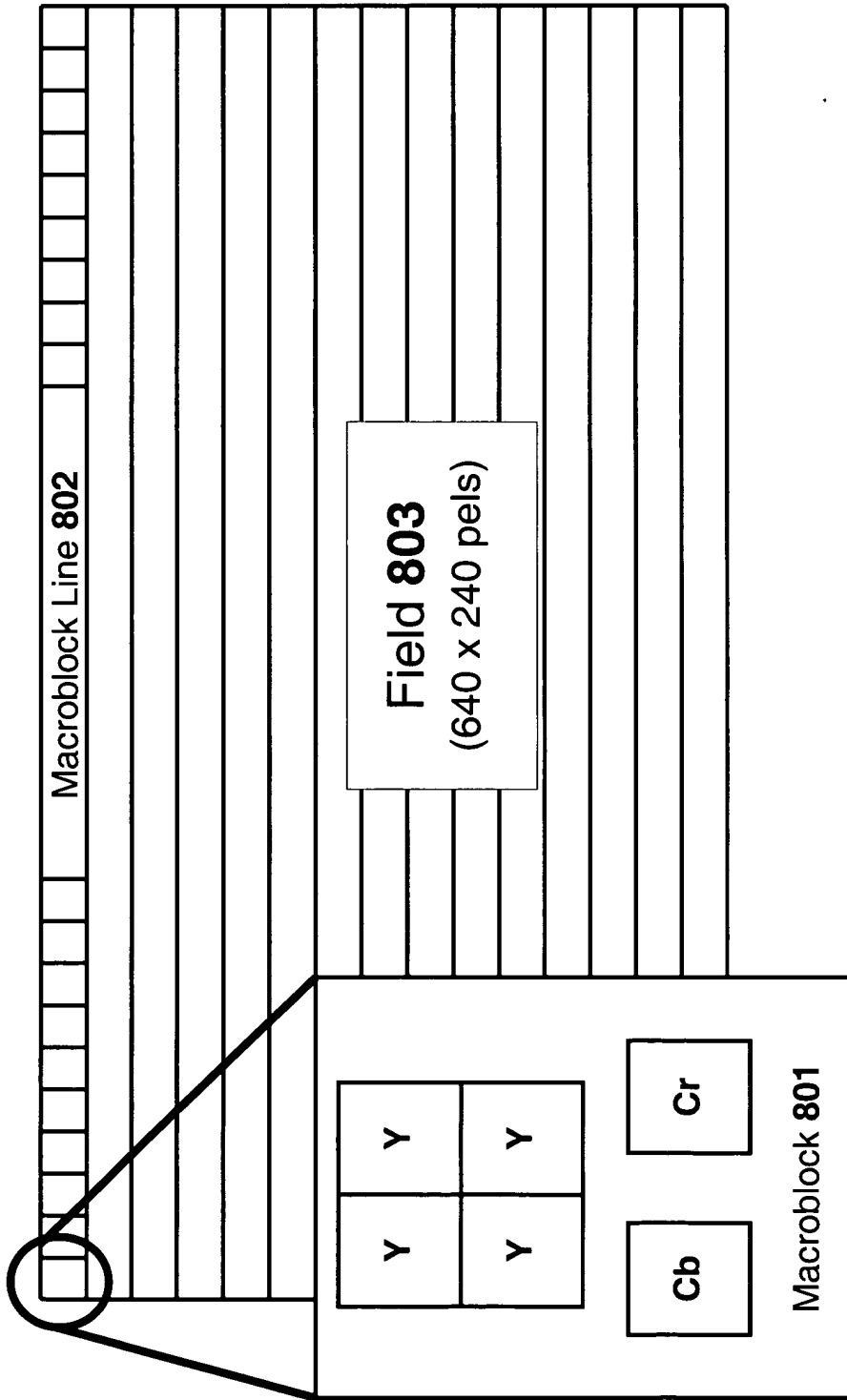


FIG. 8
(prior art)

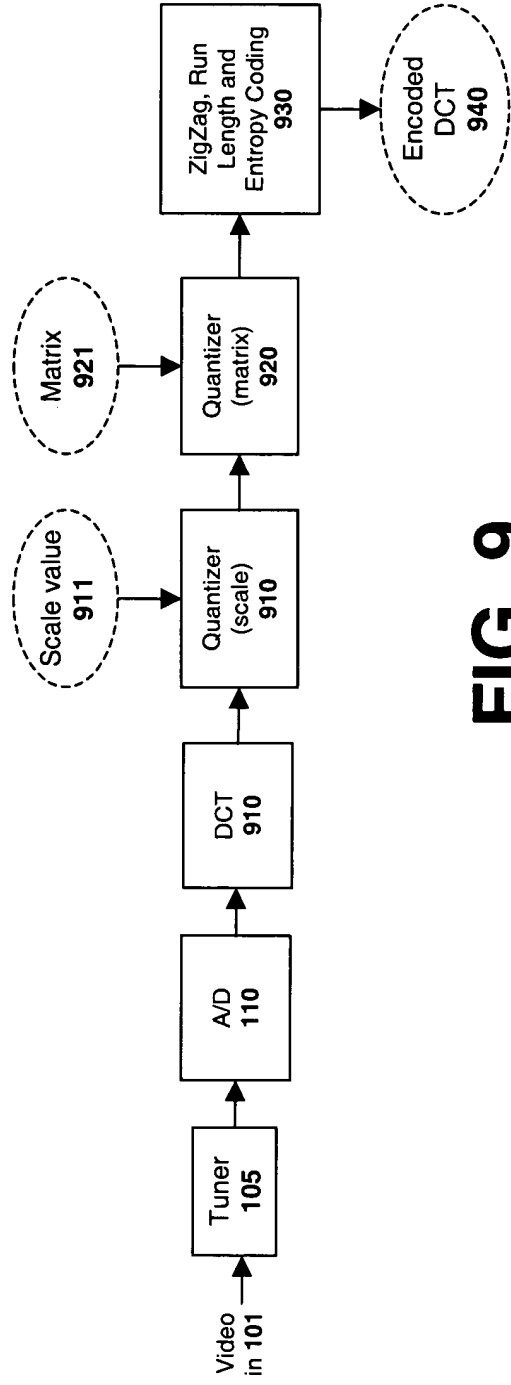


FIG. 9
(prior art)

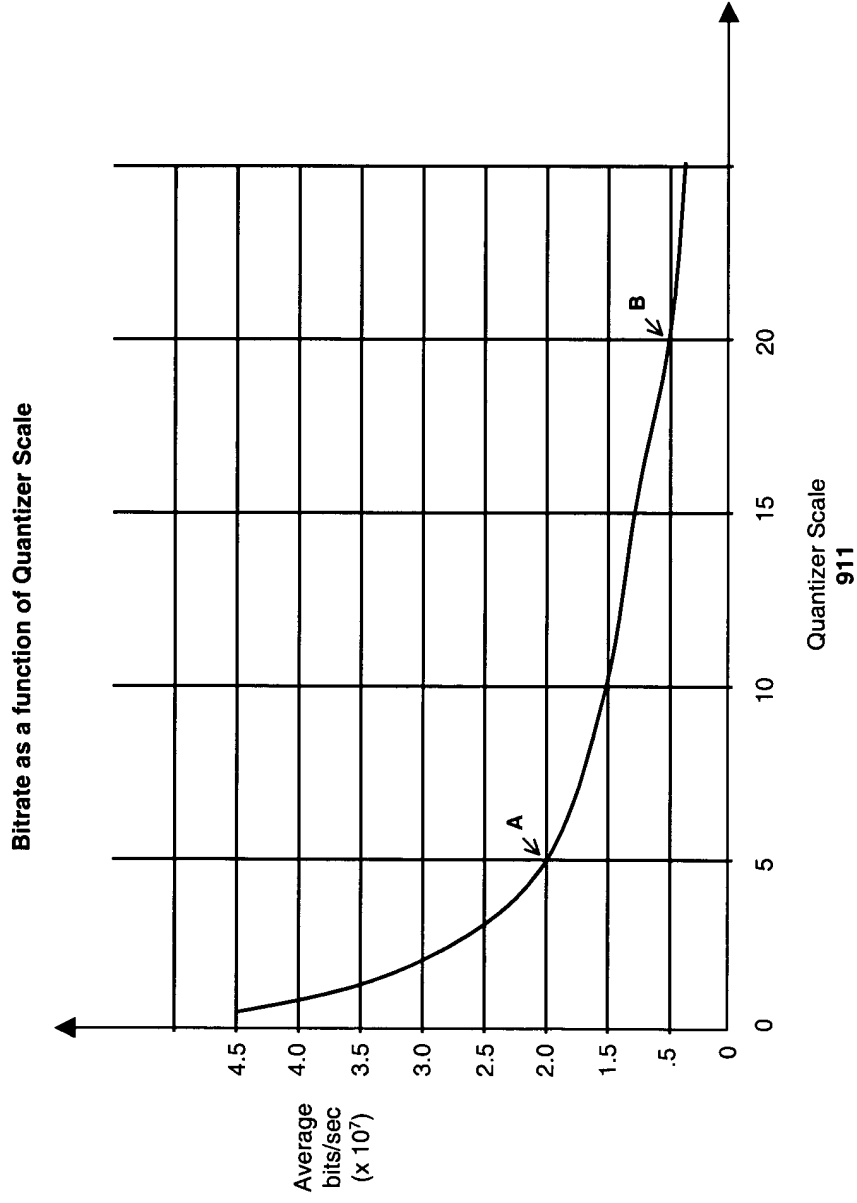


FIG. 10

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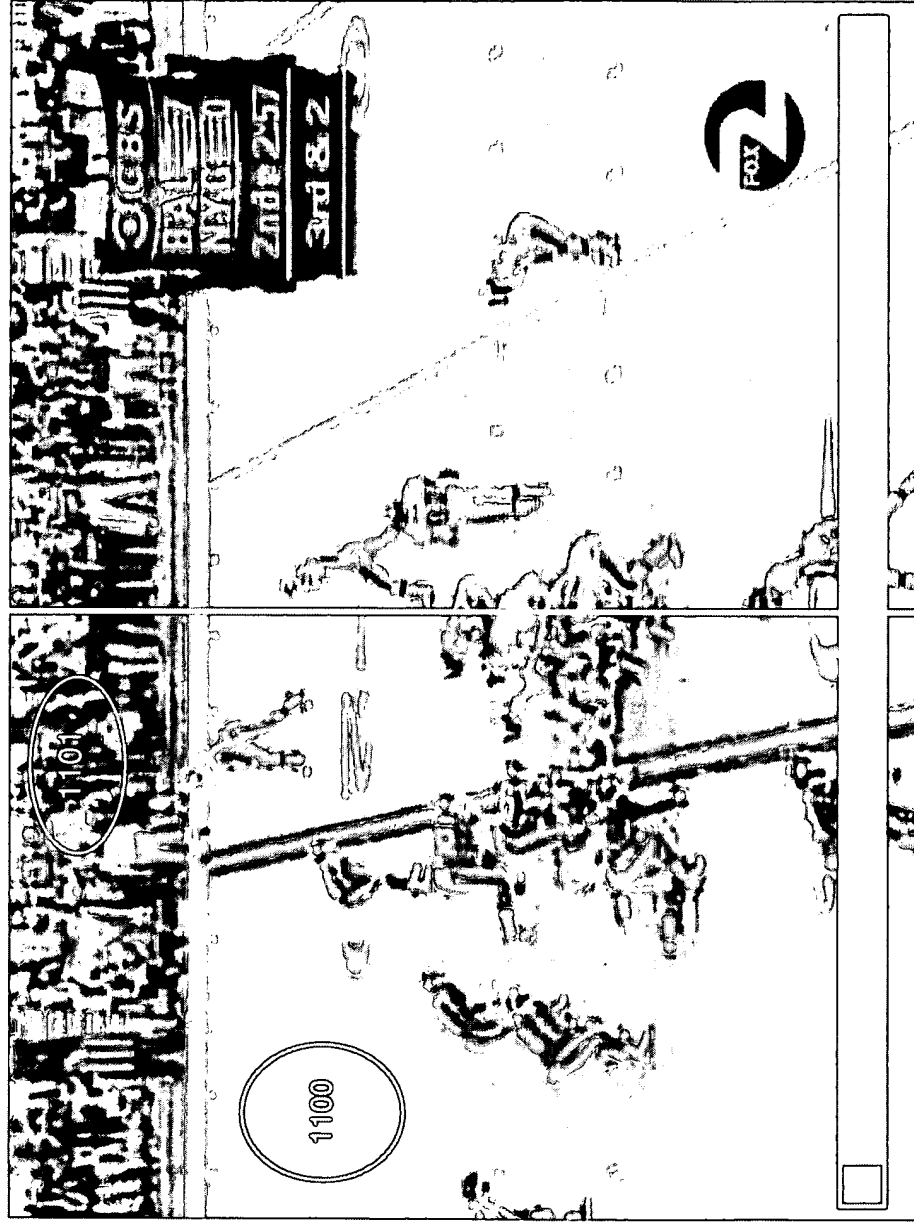


FIG. 11

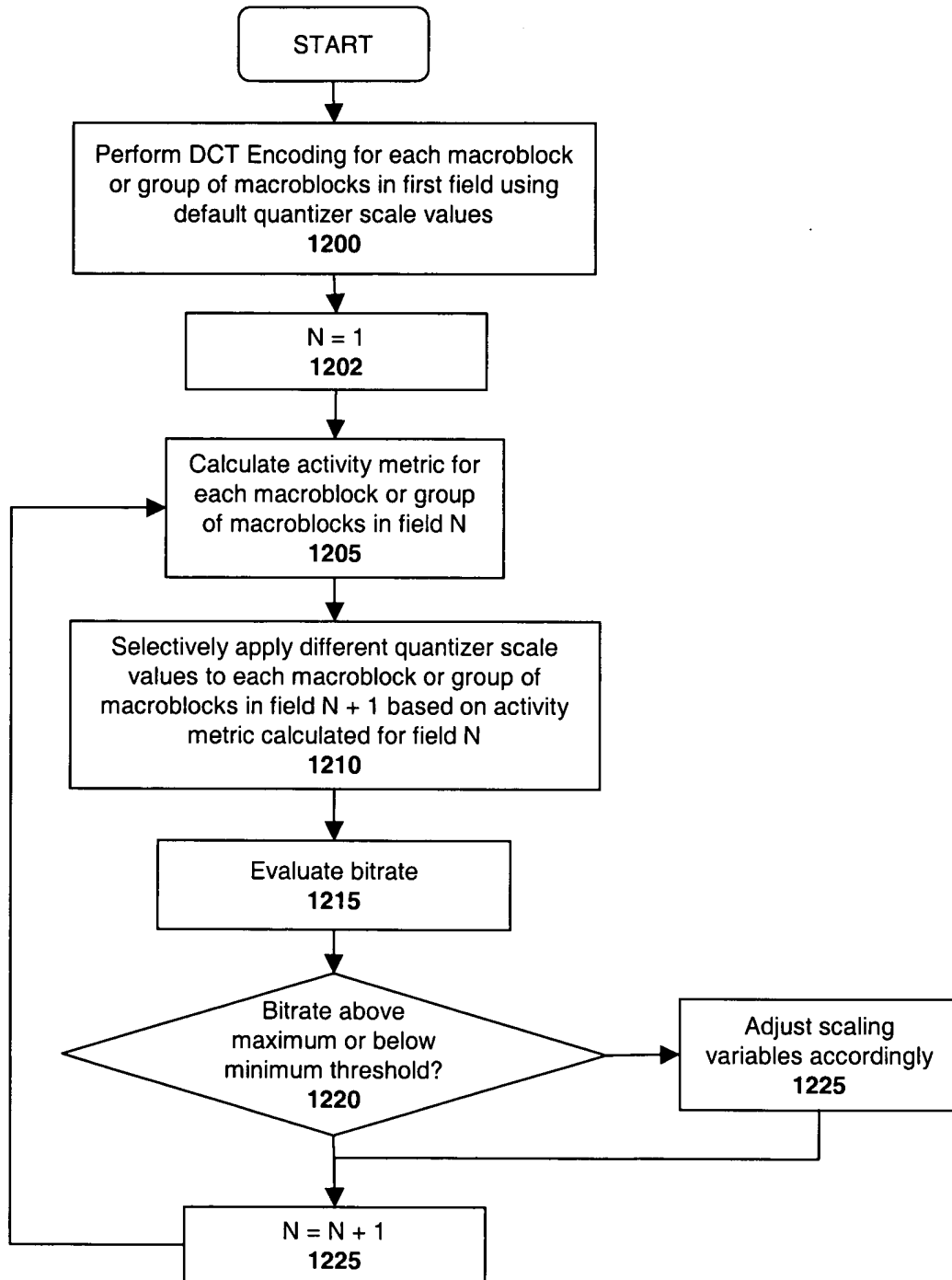


FIG. 12

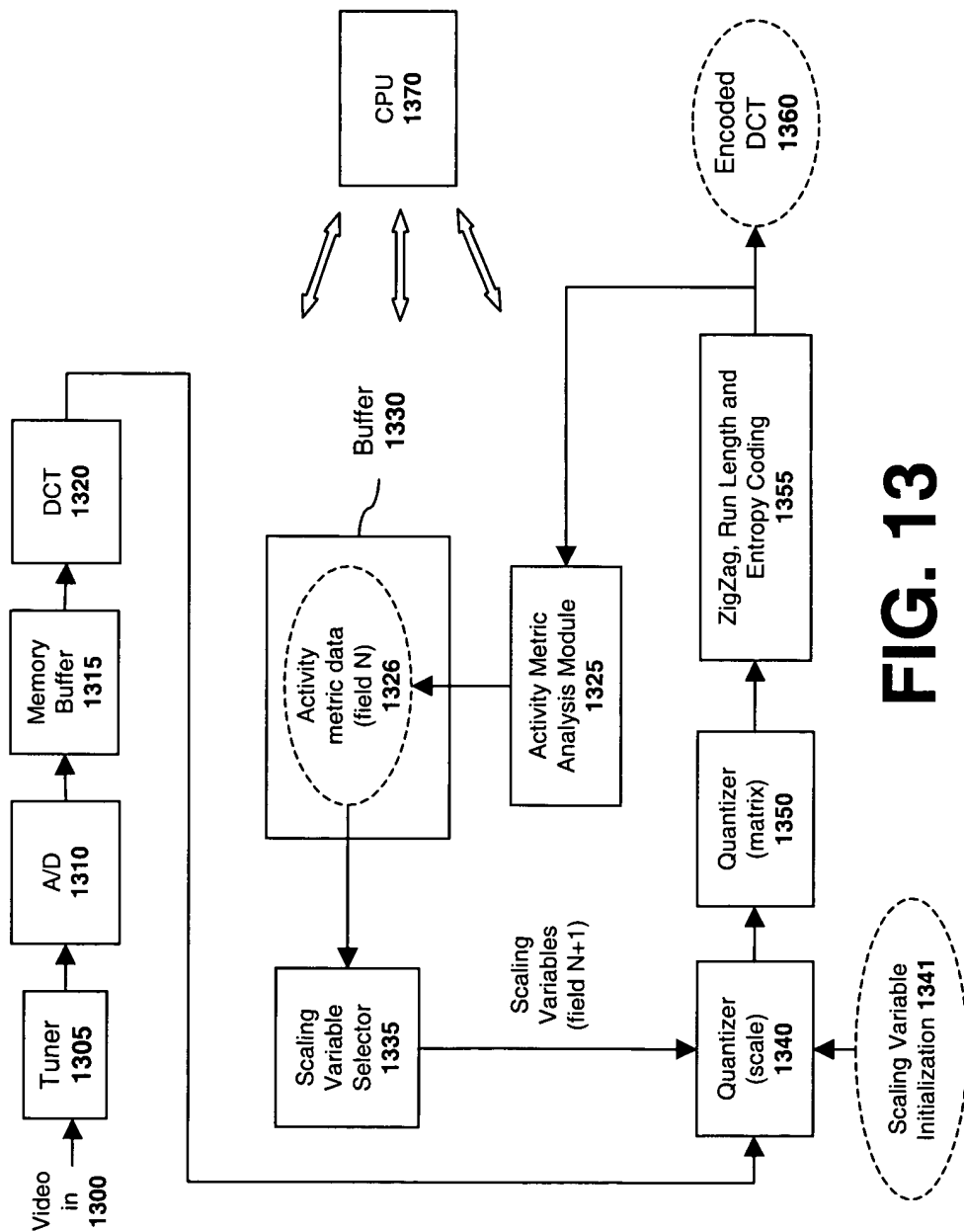


FIG. 13

FIG. 14

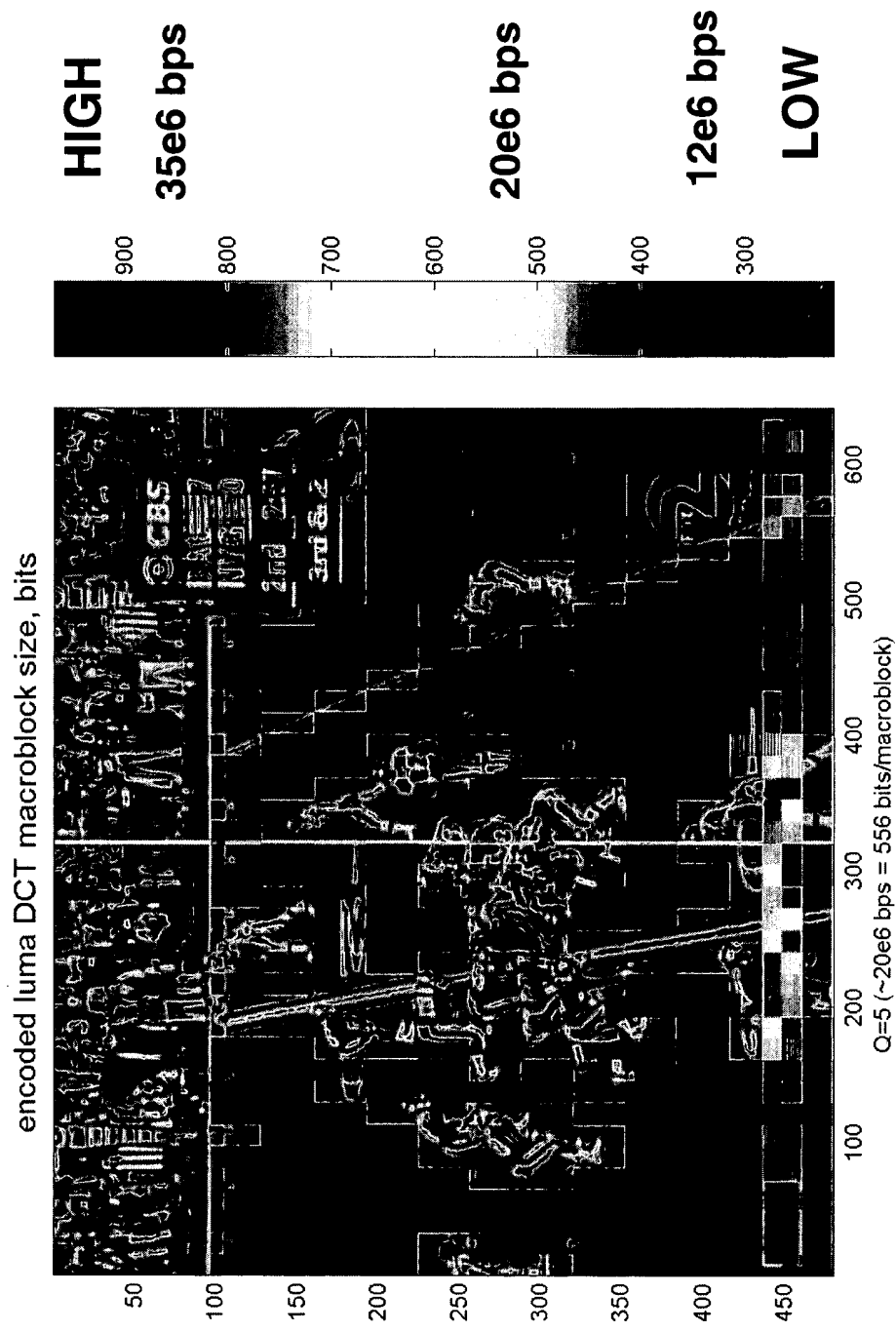


FIG. 14

Bit Allocation Hierarchy

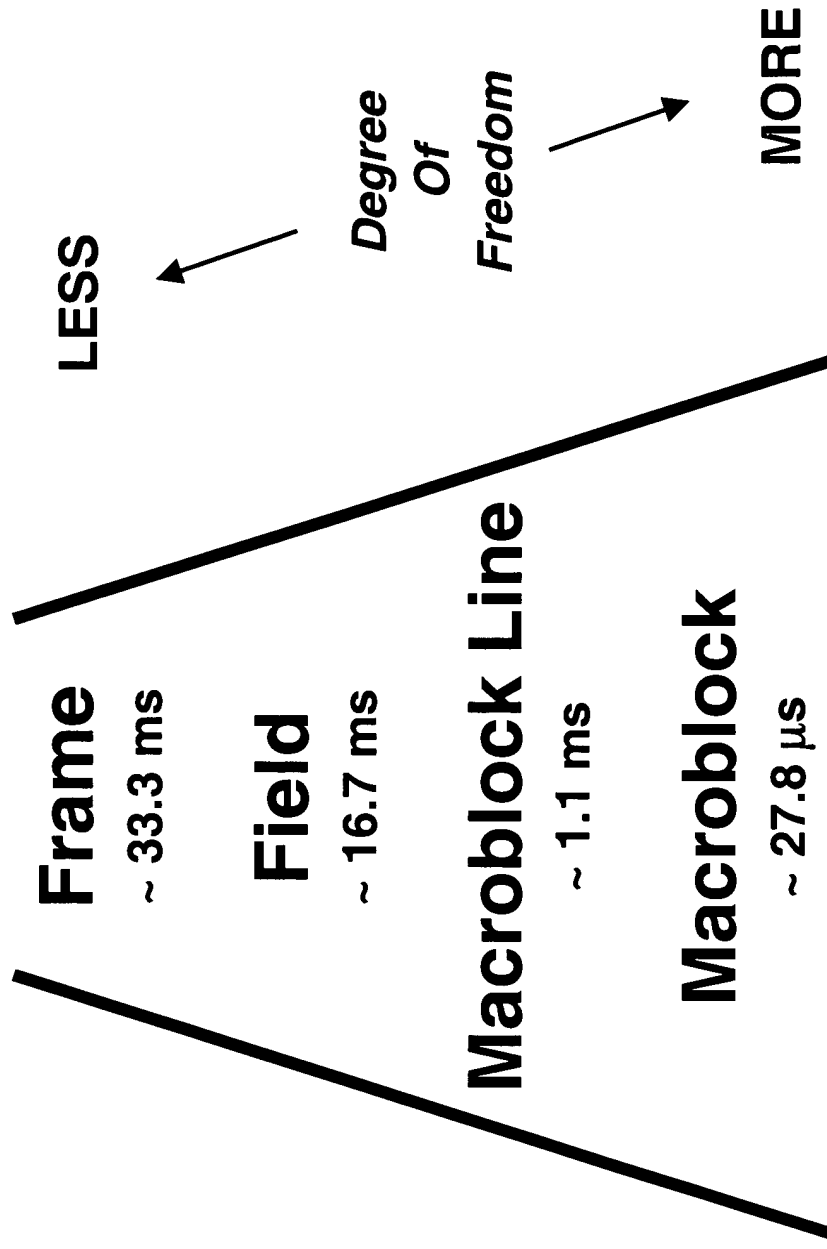


FIG. 15